

IAP12 Rec'd PCT/PTO 19 JUN 2006

**APPLICATION DATA SHEET****Application Information**

Application number::

Filing Date::

Application Type::

Regular

Subject Matter::

Utility

Suggested classification::

Suggested Group Art Unit::

CD-ROM or CD-R?::

None

Number of CD disks::

Number of copies of CDs::

Sequence submission?::

Computer Readable Form (CRF)?::

No

Number of copies of CRF::

Title ::

MONOLITHIC SEMICONDUCTOR  
INTEGRATED CIRCUIT AND METHOD FOR  
SELECTIVE MEMORY ENCRYPTION AND  
DECRYPTION

Attorney Docket Number::

851963.425USPC

Request for Early Publication?::

No

Request for Non-Publication?::

No

Suggested Drawing Figure::

Total Drawing Sheets::

Small Entity?::

No

Petition included?::

No

Petition Type::

Licensed U.S. Gov't Agency::

Contract or Grant No::

Secrecy Order in Parent Appl.?::

No

**First Applicant Information**

Applicant Authority Type::	Inventor
Primary Citizenship Country::	Great Britain
Status::	Full Capacity
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Middle Name::	
Family Name::	Dellow
Name Suffix::	
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State or Province of Residence::	
Country of Residence::	Great Britain
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Country of mailing address::	Great Britain
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**Second Applicant Information**

Applicant Authority Type::	Inventor
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**Correspondence Information**

Correspondence Customer Number :: **38106**

**Representative Information**

Representative Customer Number::		<b>38106</b>
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**Domestic Priority Information**

Application ::	Continuity Type::	Parent Application::	Parent Filing Date::
This Application	National Stage of	PCT/GB2004/005327	12/17/04

**Foreign Priority Information**

Country::	Application number::	Filing Date::	Priority Claimed::
Europe	03258079.7	12/19/03	Yes

**Assignee Information**

Assignee name::	STMicroelectronics Limited
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City of mailing address::	Bristol
State or Province of mailing address::	
Country of mailing address::	Great Britain
Postal or Zip Code of mailing address::	BS32 4SQ

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